

Appln. No.: 09/646,665
Amendment Dated: April 14, 2004
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MAT-8014US

Amendments to the Specification:

Please replace the paragraph, beginning at page 8, line 4, with the following rewritten paragraph:

B Sync pattern generating-means 002 outputs the fixed pattern 'e3cbaa' at the clock next to 0, 1, 2 of control signal 3003, and outputs the variable pattern A '4ceacd7a81' at the clock next to 3, 4, 5, 6, or 7 of control signal 3003 and when control signal ~~004-3004~~ stays logic level L. When control signal ~~004-3004~~ stays at logic level H and control signal 3003 takes a value of 3, 4, 5, 6, or 7, generating-means 002 outputs variable pattern B 'cd7aea814c' at the clock next to the respective values. Sync pattern generating-means 002 repeats these operations thereby outputting sync pattern A and sync pattern B alternately as signal 2003. The control method shown in Fig. 3 is an example and other control methods may be used.